

CLAIMS:

1. Record carrier comprising a first annular section with an outer perimeter comprising an first ECC block with a size and a second annular section with an inner perimeter adjacent to the outer perimeter of the first annular section, the second annular section comprising a second ECC block with a size where the size of the second ECC block
5 is larger than the size of the first ECC block,
characterized in that the outer perimeter of the first annular section is located where the size of the second ECC block is equal to a length of the inner perimeter of the second annular section.

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2. Record carrier as claimed in claim 1,
characterized in that the first ECC block is stored using a first error correction code and the second ECC block is stored using a second error correction code and that the first error correction code provides a error correction capability equal to the second error correction
15 code.

3. Method for recording information on a record carrier comprising the steps of
- recording an ECC Block in a first annular section with an outer perimeter using a first ECC block size and
20 - recording an ECC block in a second annular section with an inner perimeter adjacent to the outer perimeter of the first annular section, using a second ECC block size where the second ECC block size is larger than the first ECC block size,
characterized in that the outer perimeter of the first annular section is located where the second ECC block size is equal to a length of the inner perimeter of the second annular
25 section.

4. Method as claimed in claim 3,
characterized in that
the ECC block in the first annular section is recorded using a first error correction code and

the ECC block in the second annular section is recorded using a second error correction code and that the first error correcting code provides a error correction capability equal to the second error correction code.

5 5. Recorder for recording information on a record carrier comprising a first annular section with an outer perimeter comprising an first ECC block with a size and a second annular section with an inner perimeter adjacent to the outer perimeter of the first annular section, the second annular section comprising a second ECC block with a size where the size of the second ECC block is larger than the size of the first ECC block, the recorder
10 comprising error correction means coupled to processor means coupled to writing means characterized in that the processor means is operative to position the outer perimeter of the first annular section where the second ECC block size is equal to a length of the inner perimeter of the second annular section by providing ECC blocks to the writing means

15 6. Recorder as claimed in claim 5,
characterized in that
the processor are arranged to receive an first ECC block with a first error correction code from the error correction means when recording the first ECC block in the first annular section and processor means is further arranged to receive a second ECC block with a second
20 error correction code from the error correction means when recording the second ECC block in the second annular section and that the error correction capability of the first error correction code is equal to the second error correcting code.